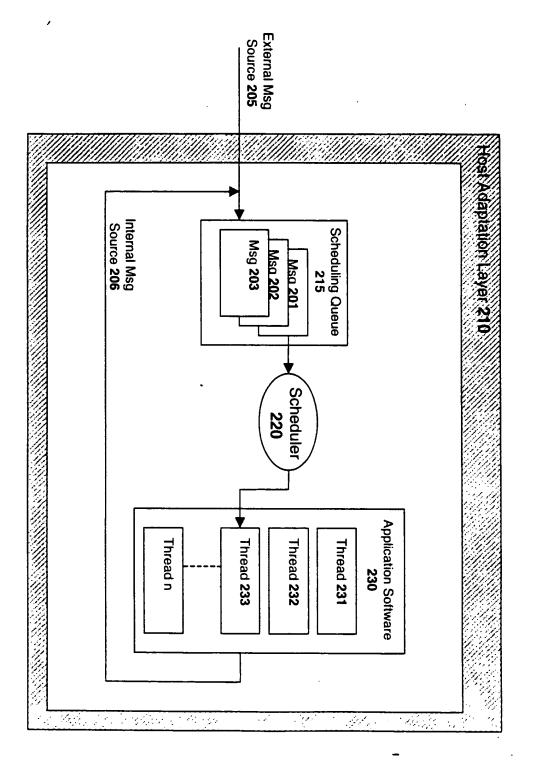


FIG. 1

* # * * * *



Processor 350	Application Program 340	PTE Application Interface 330	Host Adaptation Layer 320	Operating System 310

FIG. 3

Part of the second

Processor 450	Application Program 440	PTE Application Interface 430	Host Adaptation Layer 420

FIG. 4

12 March 1

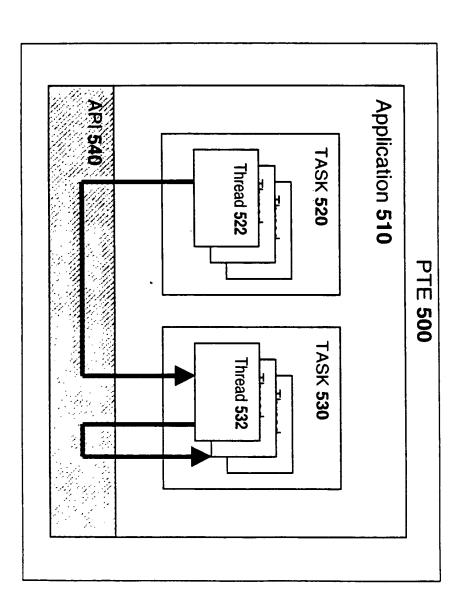
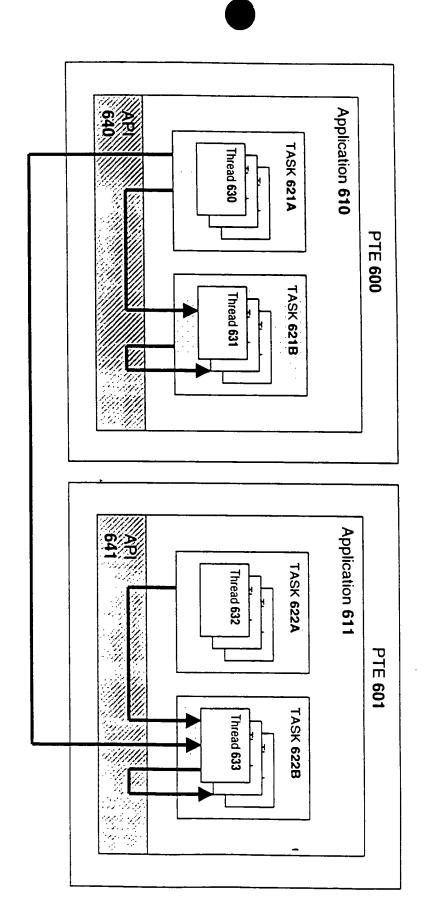


FIG. 5

** *** · ·



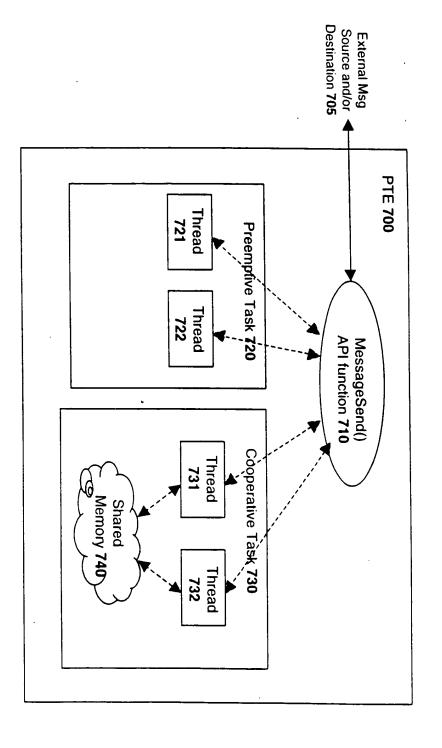
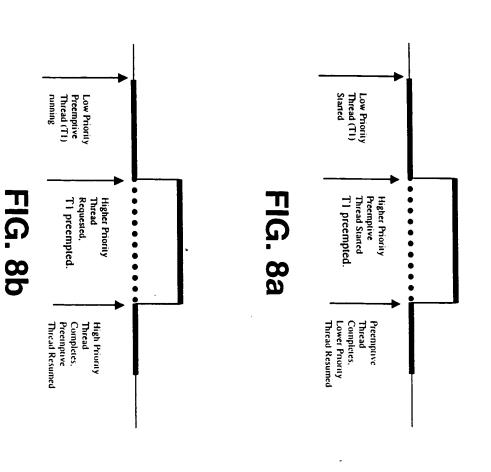
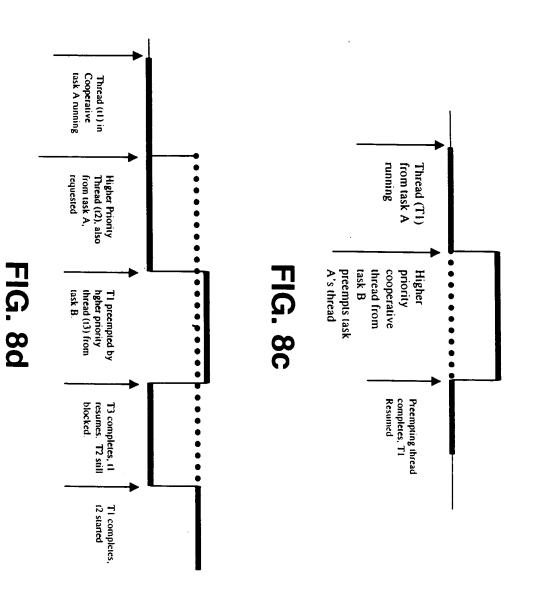


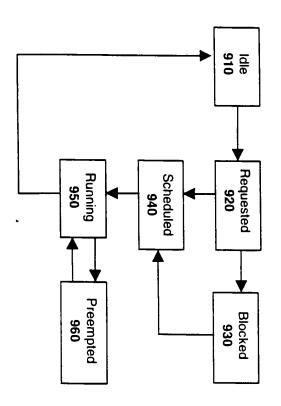
FIG. 7

7 M. .



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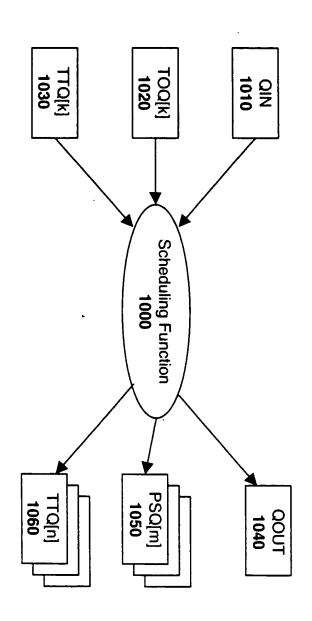
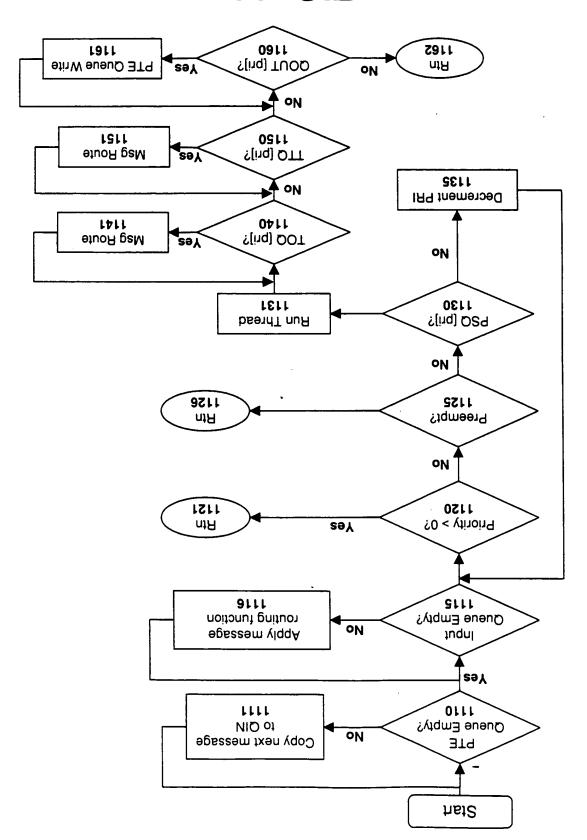


FIG. 10

4. A. ...

FIG. 11



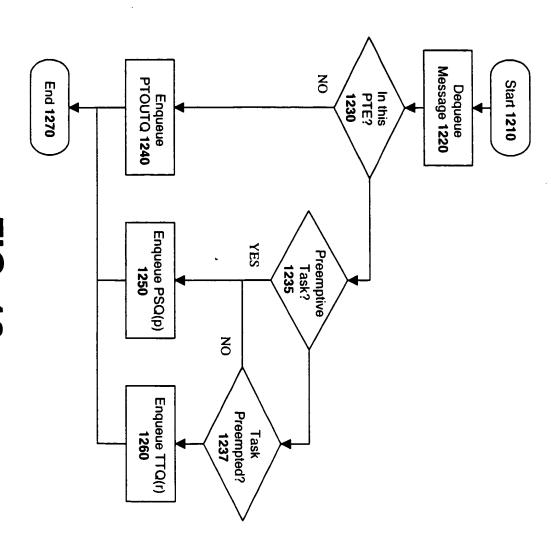


FIG. 12

3 Marie .

FIG 13c

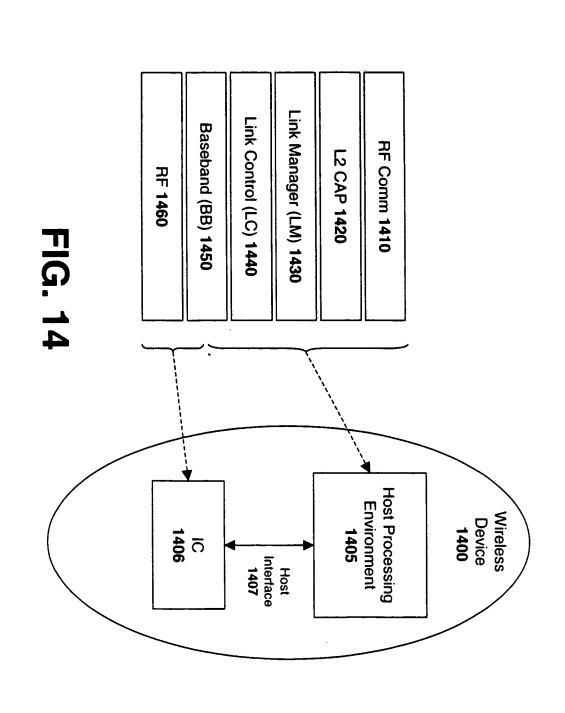
"	Max-Priority
"	"
**	Ī
{əsn-u ə p) snıvıS	0
States	Priority Level
ed Task Table	1qm99Tq

EIG 13P

sk Status Table	вТ
Status	Task(m)
Status (Idle 0 MaxPriority)	0
,,	I
	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
,,	W

FIG 138

Task Type (preempiive cooperative)	
Thread Priority (0-MaxPriority)	
Thread ID	
Task ID	Thread[n]
Thread Attribute Table	



** # ** · ·



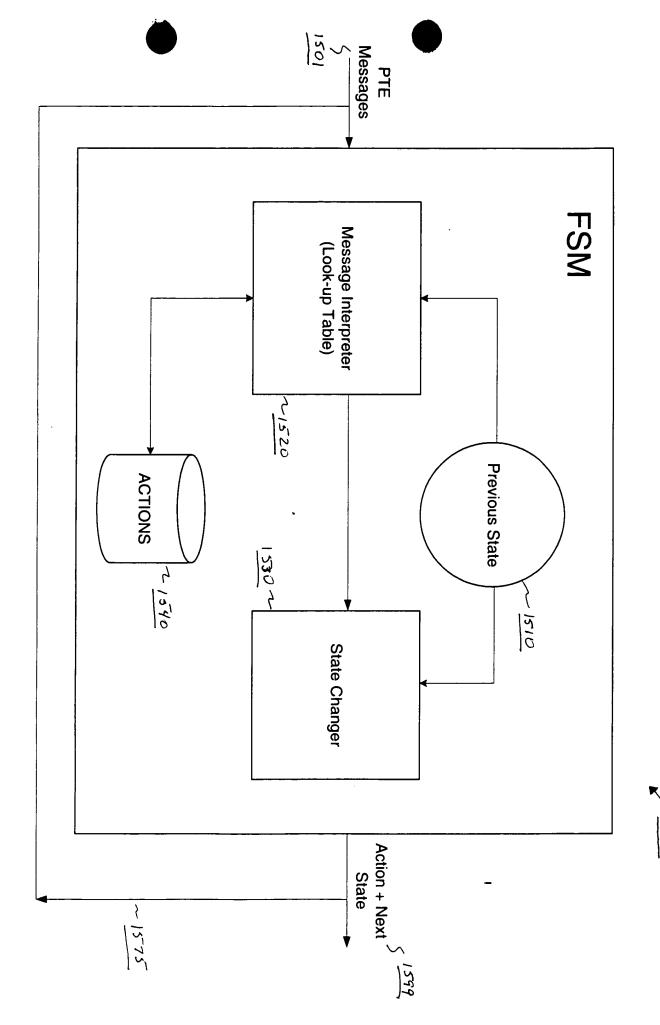


Figure 15